

FIG. 1

The diagram illustrates the interaction between three components: 10-1 REAL TIME PROCESSOR, 14-1 PROCESSING QUEUE, and 11 NON-REAL TIME PROCESSOR.

10-1 REAL TIME PROCESSOR: This processor receives five communication data inputs (COMMUNICATION DATA 1 through 5) at points P1, P2, P3, P4, and P5. It sends data to the processing queue at P6. A bracket labeled P7 groups the first three communication data inputs.

14-1 PROCESSING QUEUE: This queue receives data from the real time processor at P6. It contains a box labeled "PARAMETER, COMMUNICATION DATA, ETC." and sends "DEMAND DATA ABSENT" and "DEMAND DATA PRESENT" signals to the non-real time processor. It also receives a "QUEUE READ" signal from the non-real time processor at P8.

11 NON-REAL TIME PROCESSOR: This processor initiates the process with a "POLLING" signal to the queue. It receives "DEMAND DATA ABSENT" and "DEMAND DATA PRESENT" signals. It performs a "QUEUE READ" at P8, which triggers "PROCESSING START P9". It then sends a "POLLING" signal back to the queue at P10, which triggers "PROCESSING END". A bracket labeled P7 groups the first three polling signals.

FIG. 3

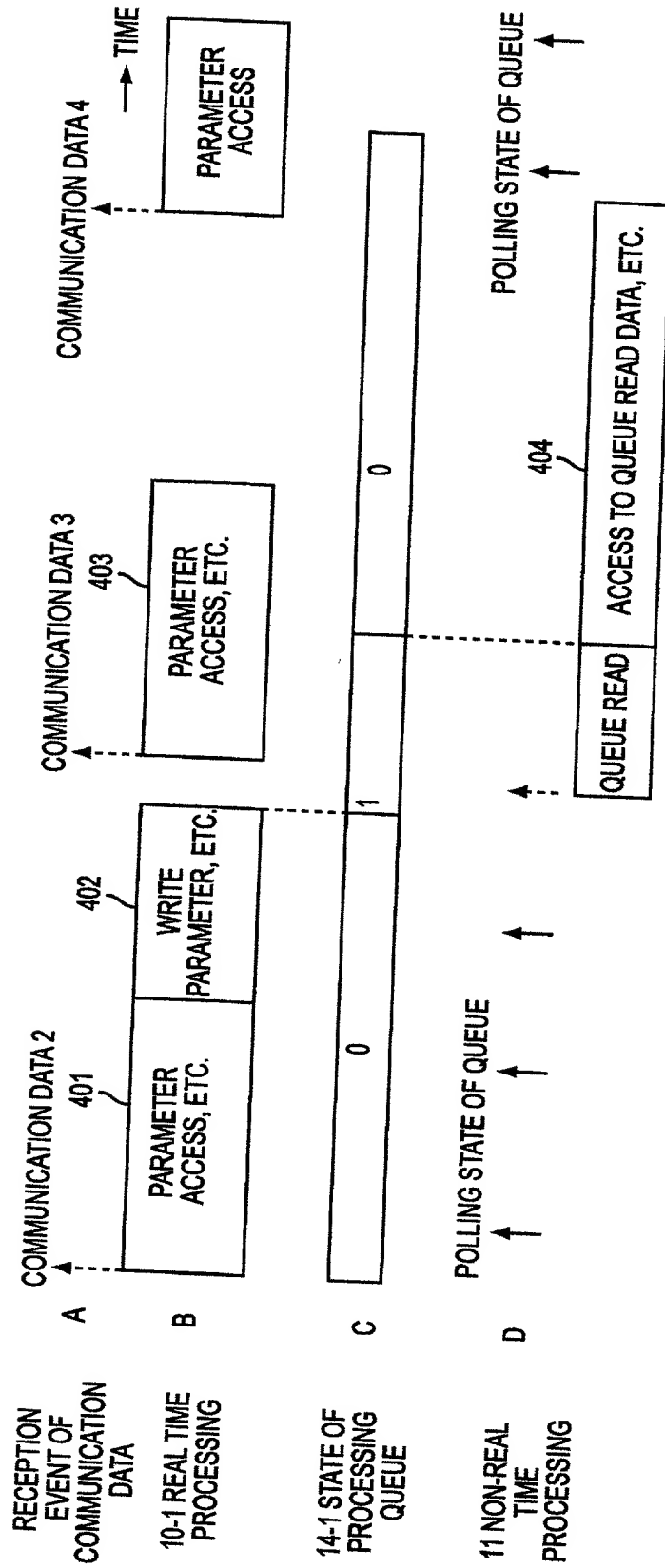


FIG. 4

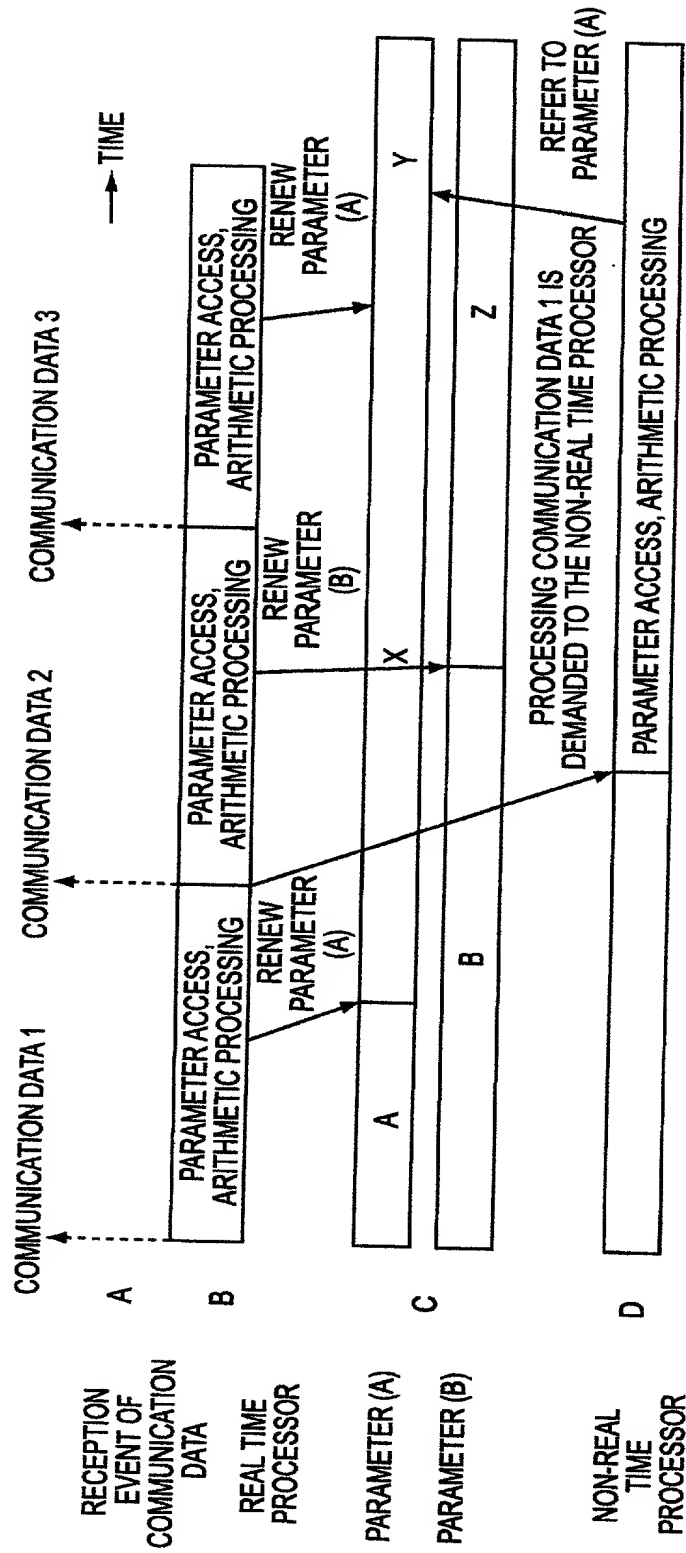


FIG. 5

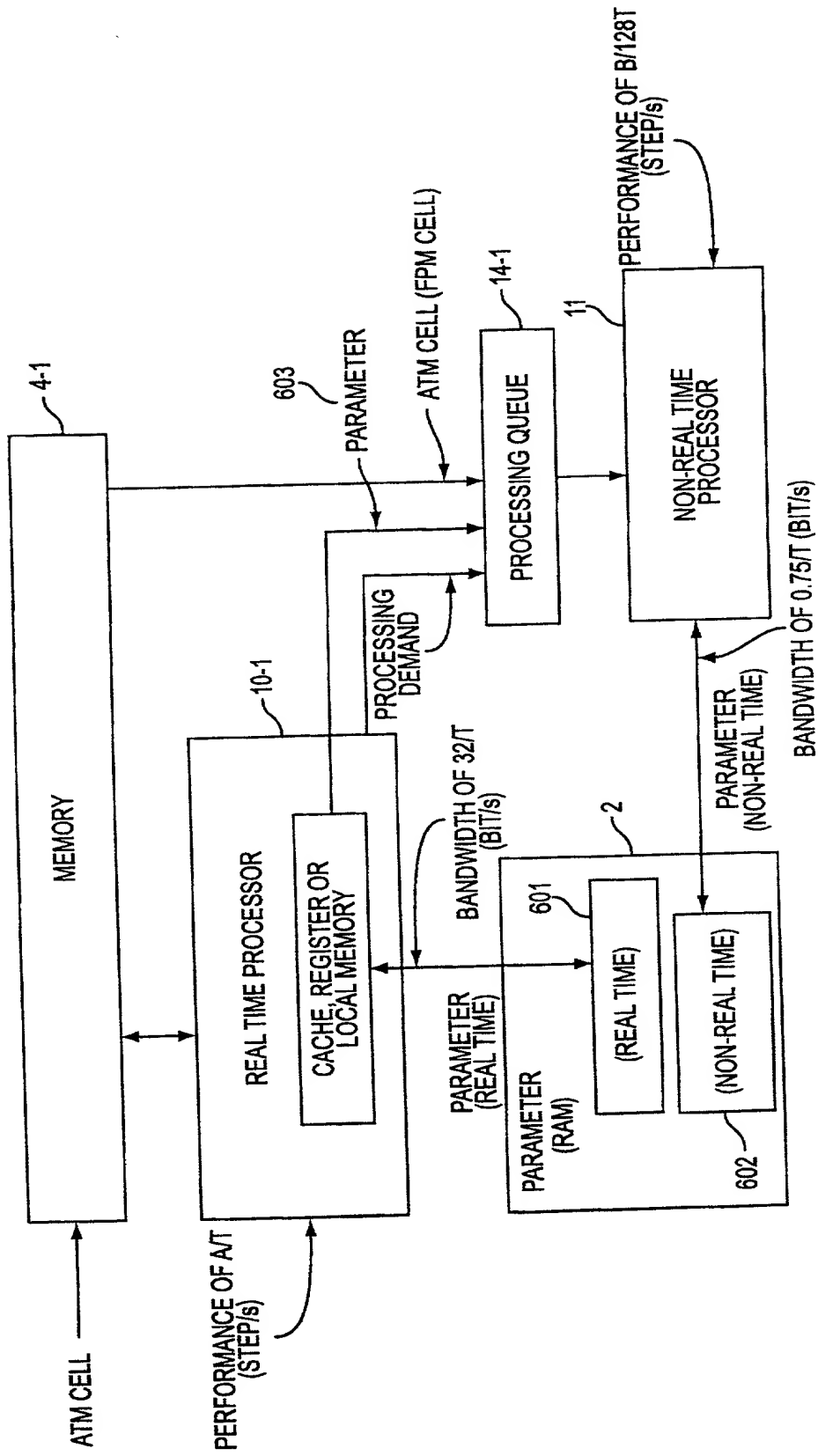


FIG. 6

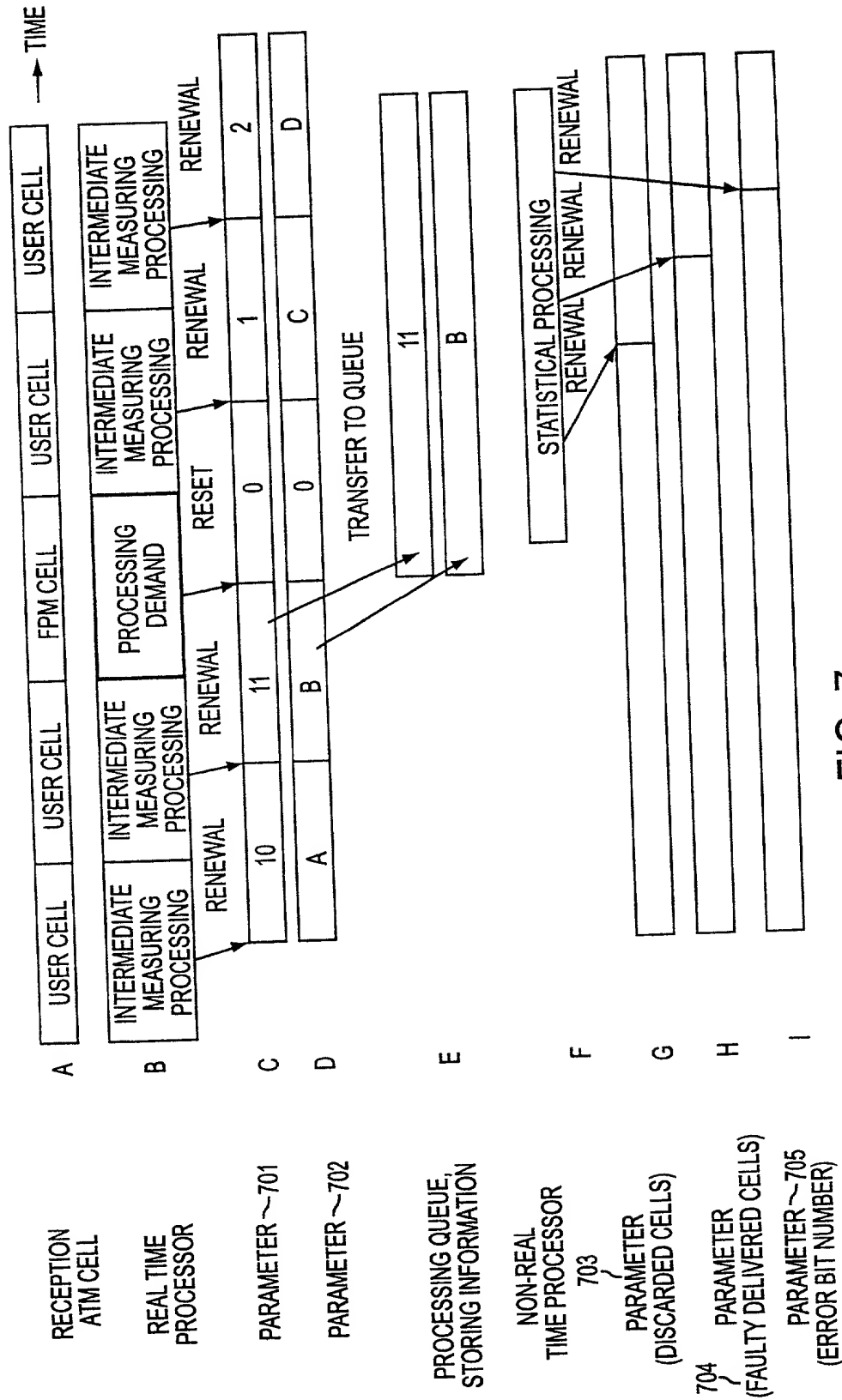
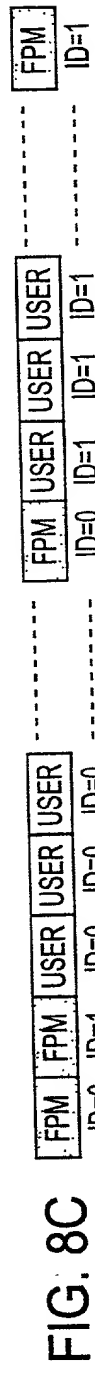
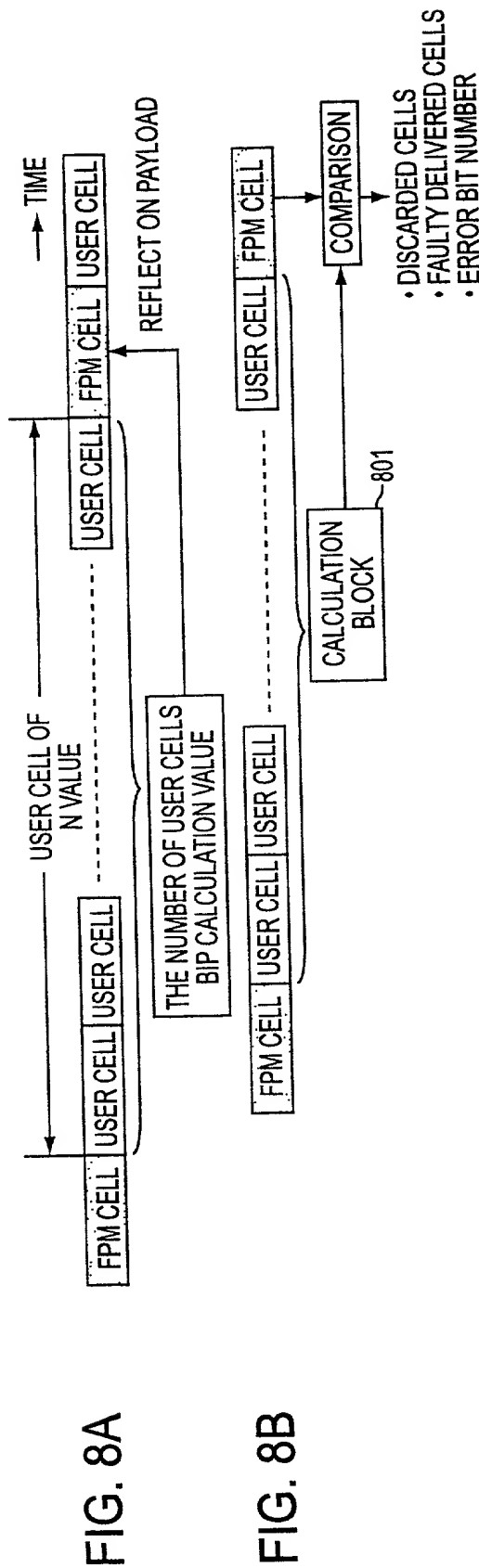
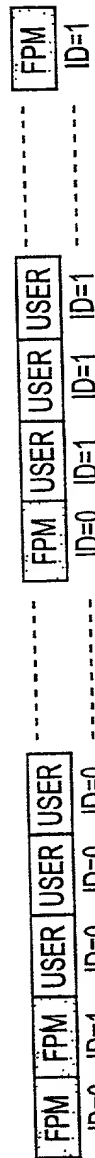


FIG. 7



CONTINUOUS RECEPTION OF FPM CELL OF ID=0 AND ID=1 WHEN 128 USER CELLS OF SAME ID ARE NOT TRANSMITTED, THE NEXT FPM CELL CANNOT BE SENT.



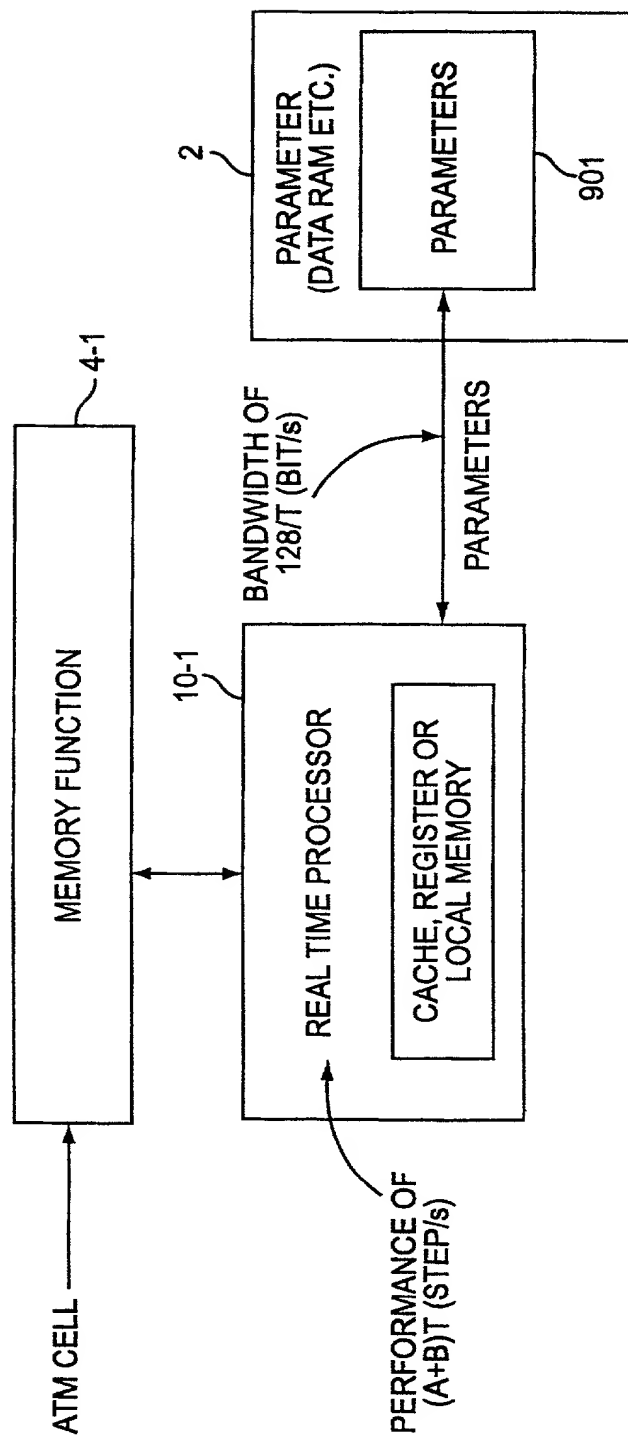


FIG. 9

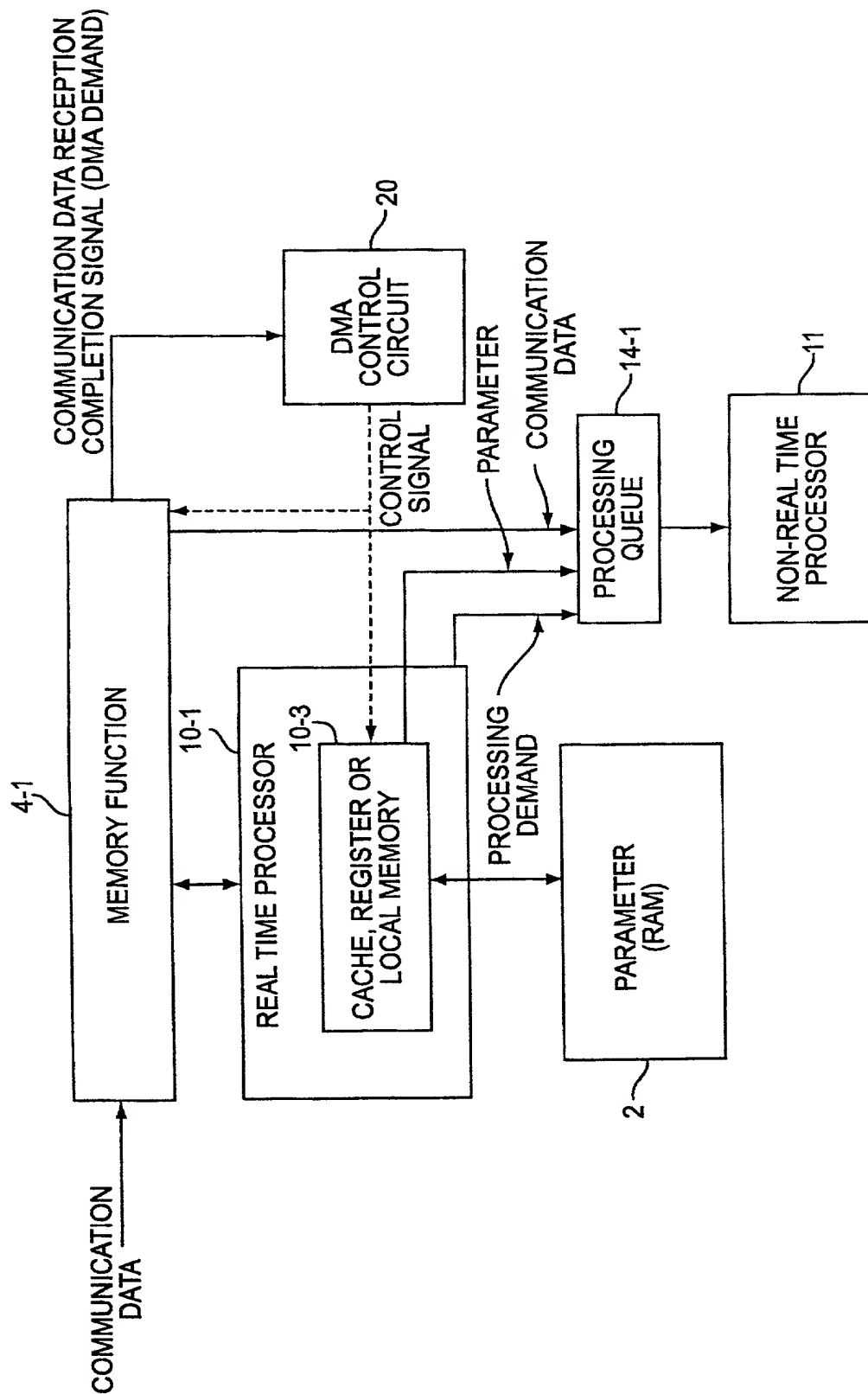


FIG. 10

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sequenceDiagram
    participant RT as 10-1 REAL TIME PROCESSOR
    participant PQ as 14-1 PROCESSING QUEUE
    participant NRT as 11 NON-REAL TIME PROCESSOR

    Note over RT: COMMUNICATION DATA 1
    RT->>PQ: DMAACTIVATION I  
COMMUNICATION DATA
    RT->>PQ: PARAMETER ACCESS  
PARAMETER
    RT->>PQ: ARITHMETIC PROCESSING S110
    Note over PQ: POLLING
    NRT->>PQ: 
    PQ->>RT: PROCESSING DEMAND ABSENT
    Note over RT: COMMUNICATION DATA 2
    RT->>PQ: DMAACTIVATION II  
COMMUNICATION DATA
    RT->>PQ: PARAMETER ACCESS  
PARAMETER
    RT->>PQ: ARITHMETIC PROCESSING
    RT->>PQ: PROCESSING DEMAND GENERATION III  
PROCESSING DEMAND
    PQ->>RT: INCREASE WRITE POINTER  
POLLING
    NRT->>PQ: 
    PQ->>RT: S111  
PROCESSING DEMAND PRESENT
    PQ->>NRT: INCREASE WRITE POINTER  
IV PROCESSING START
    
```

The diagram illustrates the interaction between three components: a 10-1 REAL TIME PROCESSOR, a 14-1 PROCESSING QUEUE, and an 11 NON-REAL TIME PROCESSOR.

10-1 REAL TIME PROCESSOR:

- Receives **COMMUNICATION DATA 1** and **COMMUNICATION DATA 2**.
- Initiates **DMAACTIVATION I** and **DMAACTIVATION II**.
- Performs **PARAMETER ACCESS** and **ARITHMETIC PROCESSING** (labeled S110).
- Generates **PROCESSING DEMAND III** (labeled S111).

14-1 PROCESSING QUEUE:

- Initializes **WRITE POINTER=0** and **READ POINTER=0**.
- Receives **COMMUNICATION DATA** and **PARAMETER** from the 10-1 REAL TIME PROCESSOR.
- Performs **ARITHMETIC PROCESSING**.
- Responds with **PROCESSING DEMAND ABSENT** and **PROCESSING DEMAND PRESENT** (labeled S111).
- Manages the **WRITE POINTER** and performs **POLLING** and **QUEUE READ** operations.

11 NON-REAL TIME PROCESSOR:

- Initiates **IV PROCESSING START**.

FIG. 11

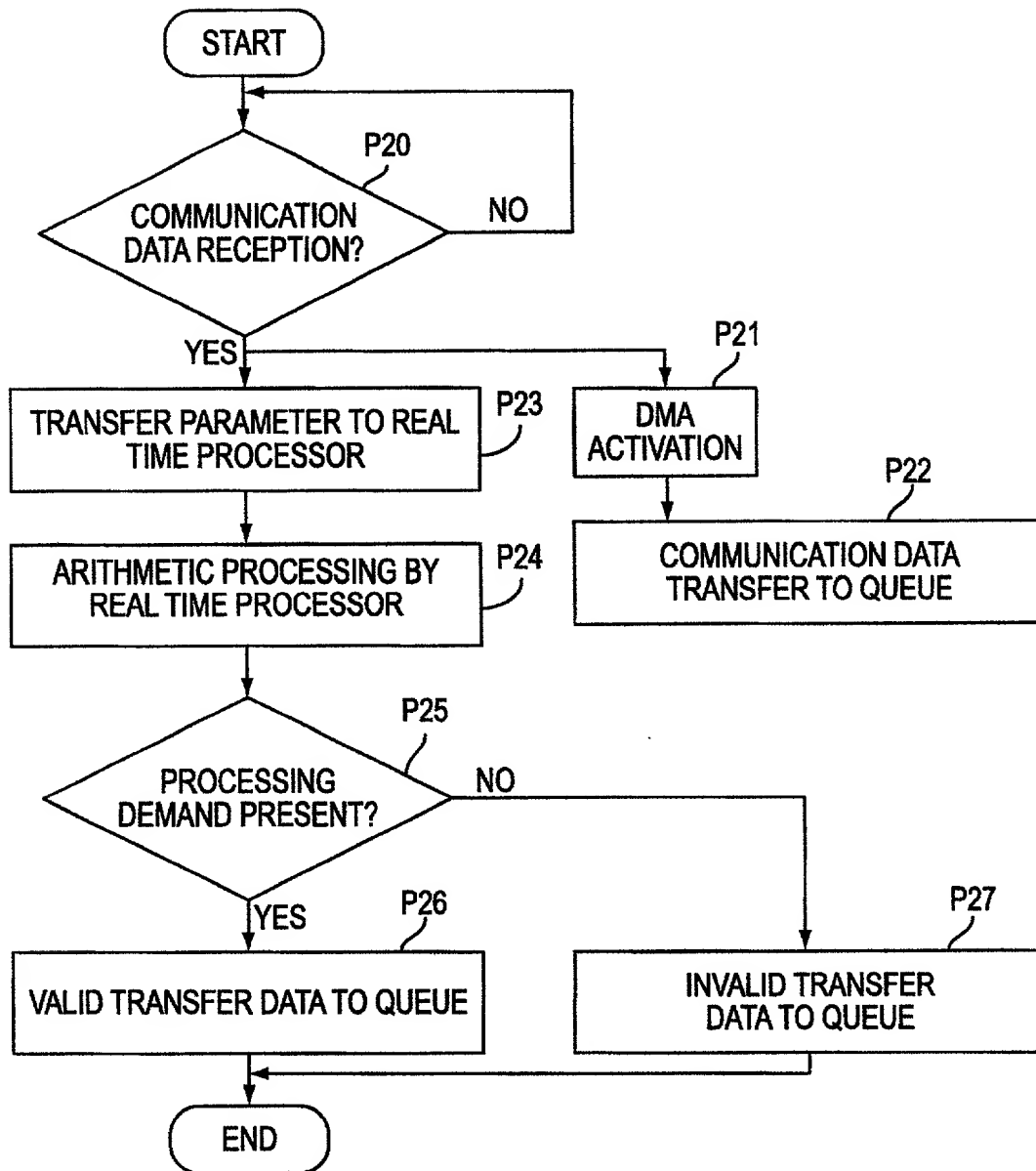


FIG. 12

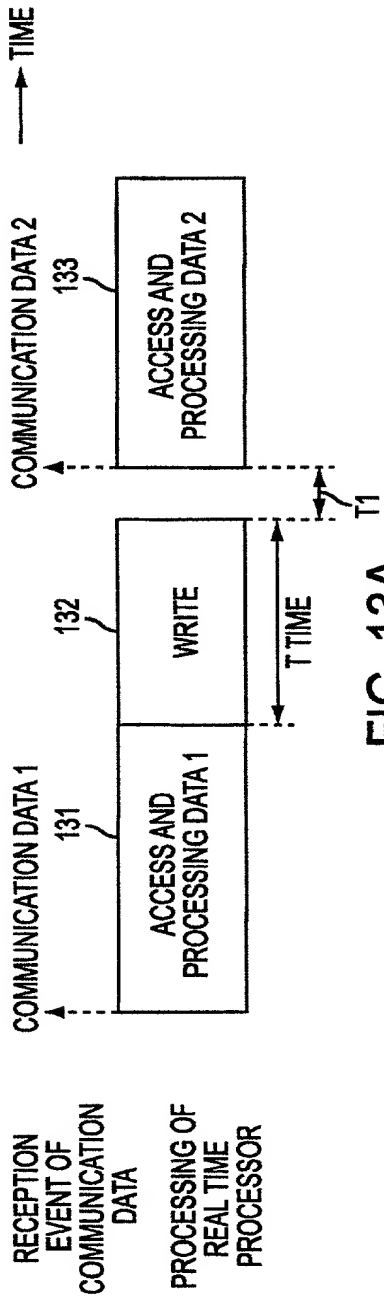


FIG. 13A

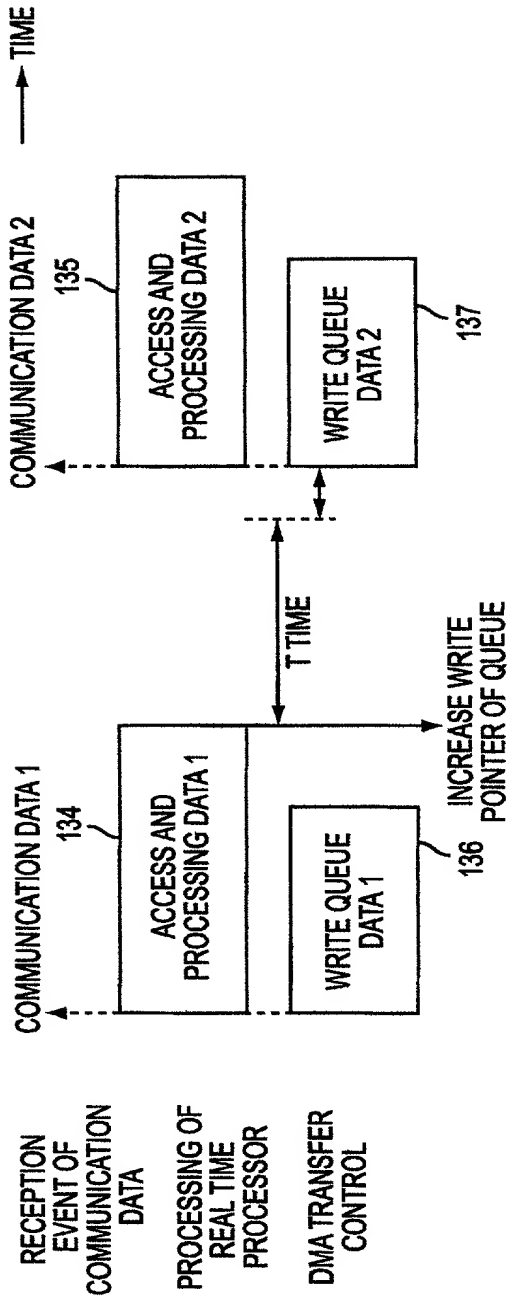


FIG. 13B

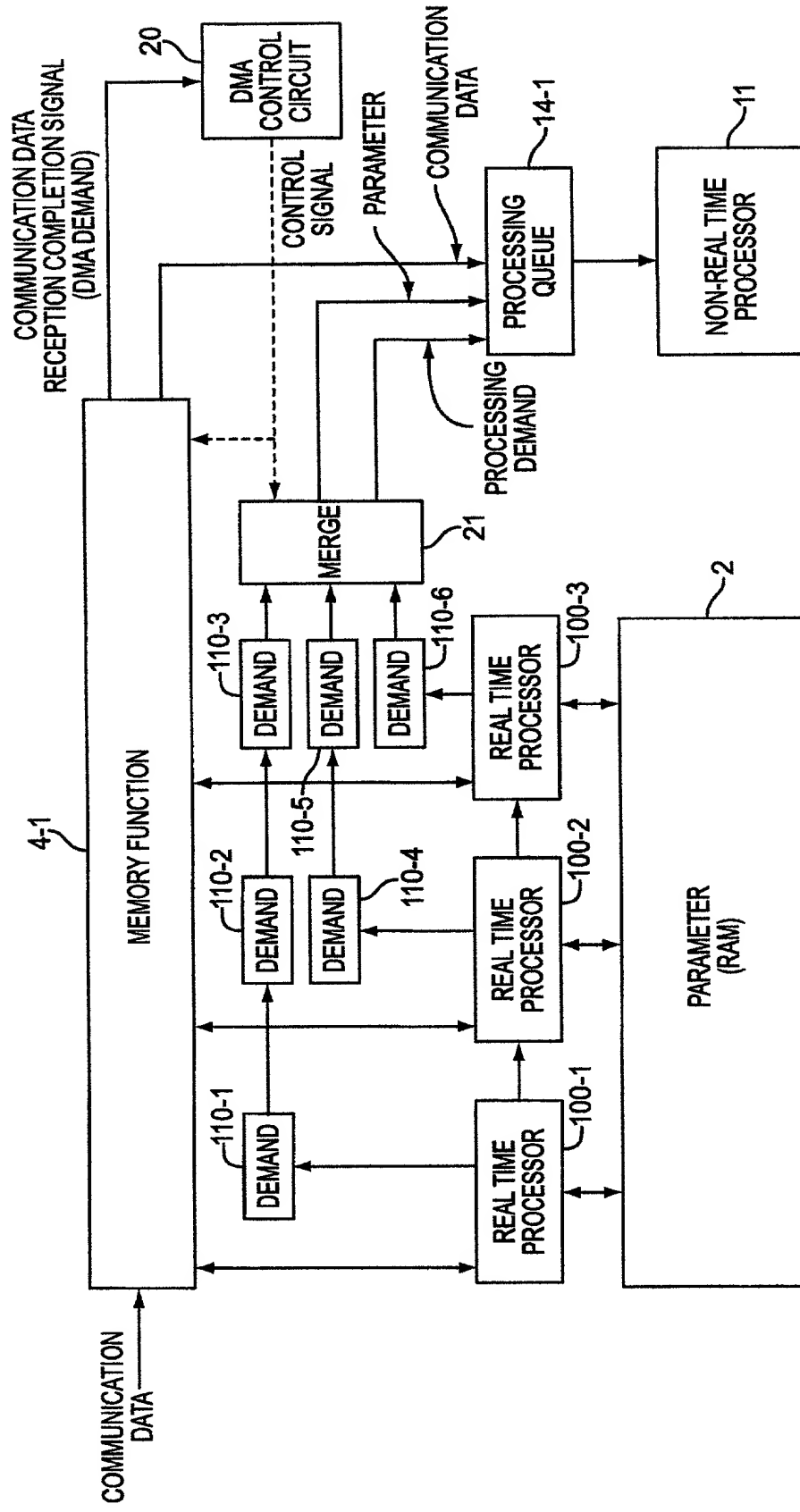


FIG. 14

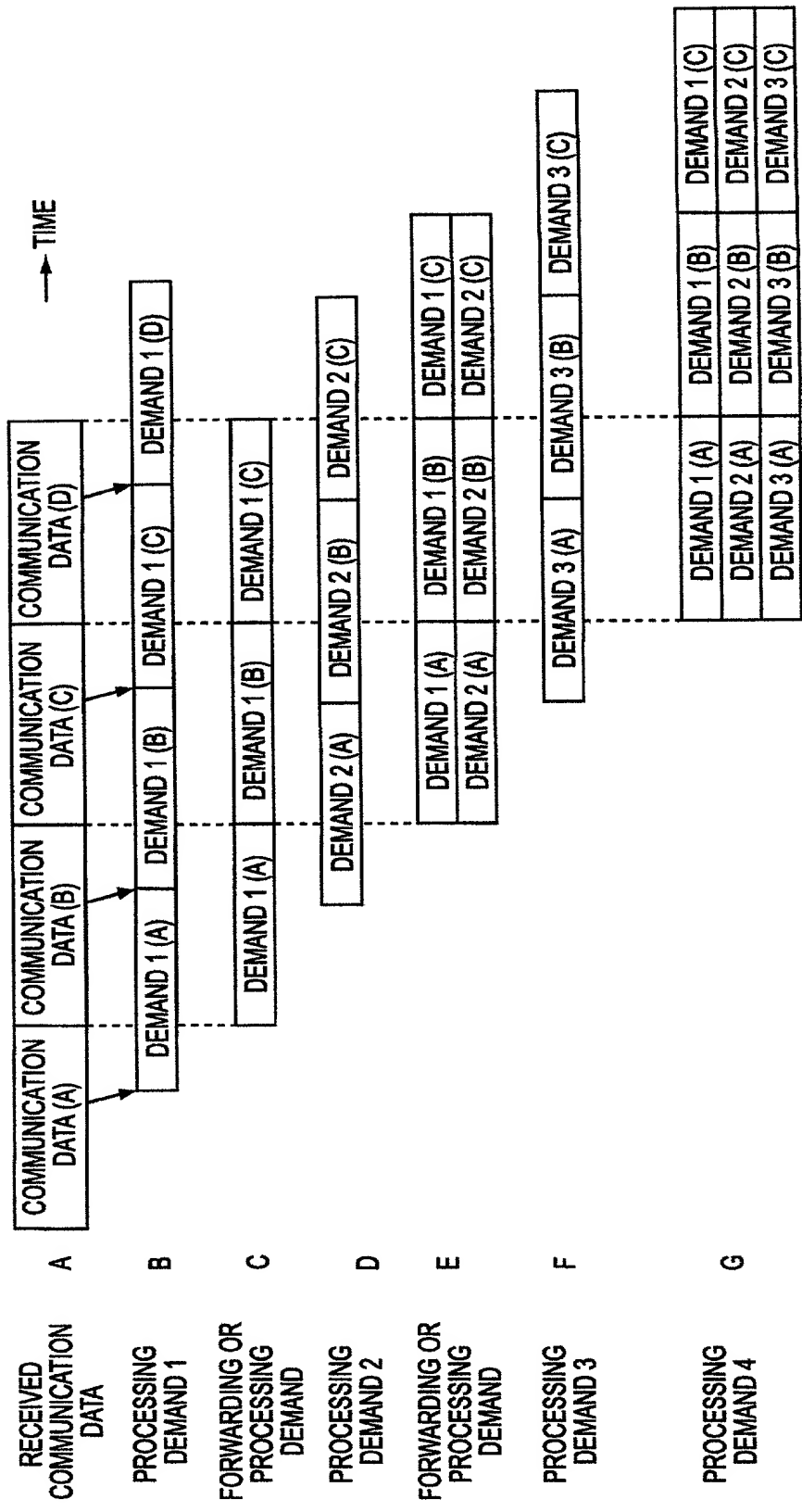


FIG. 15

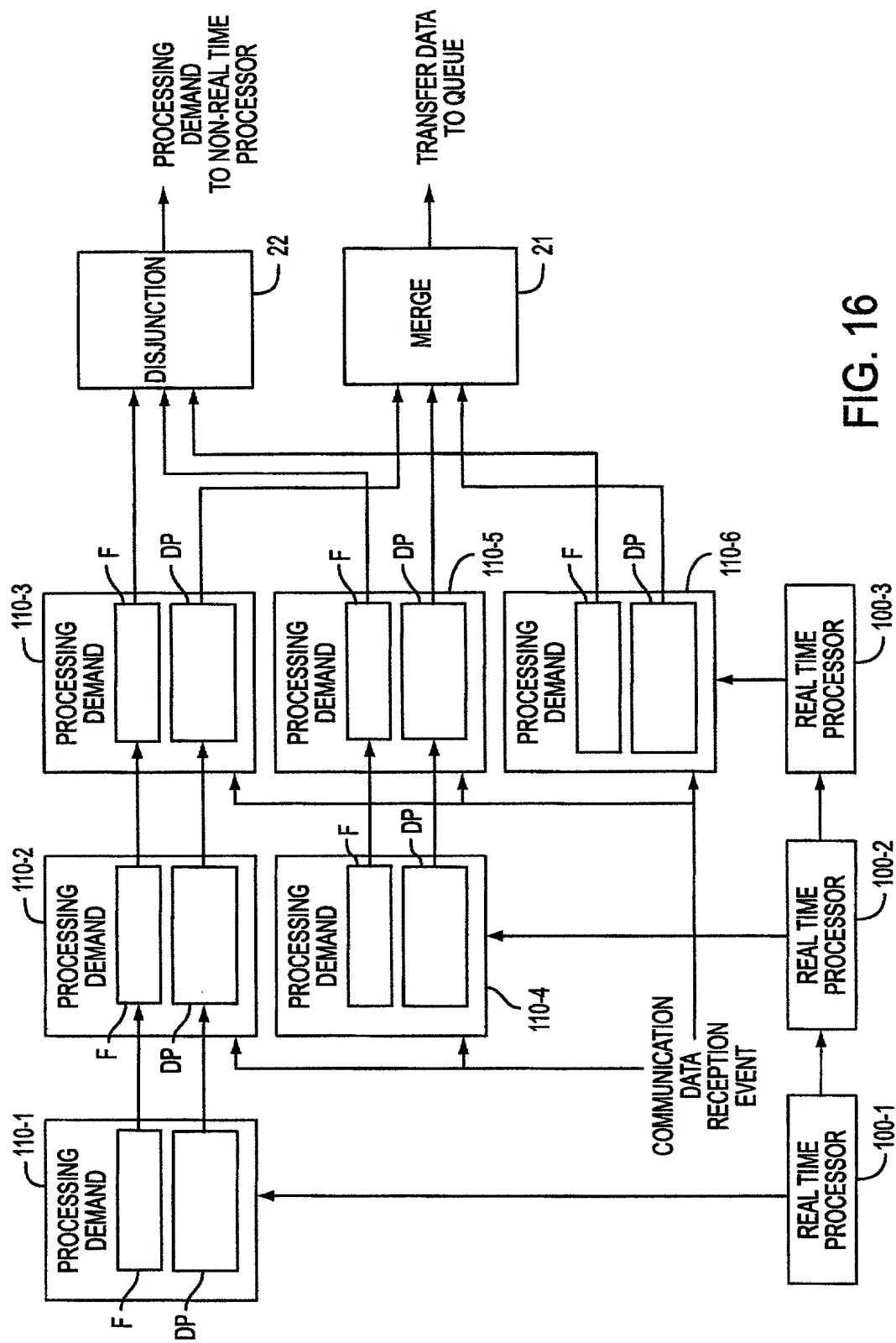


FIG. 16

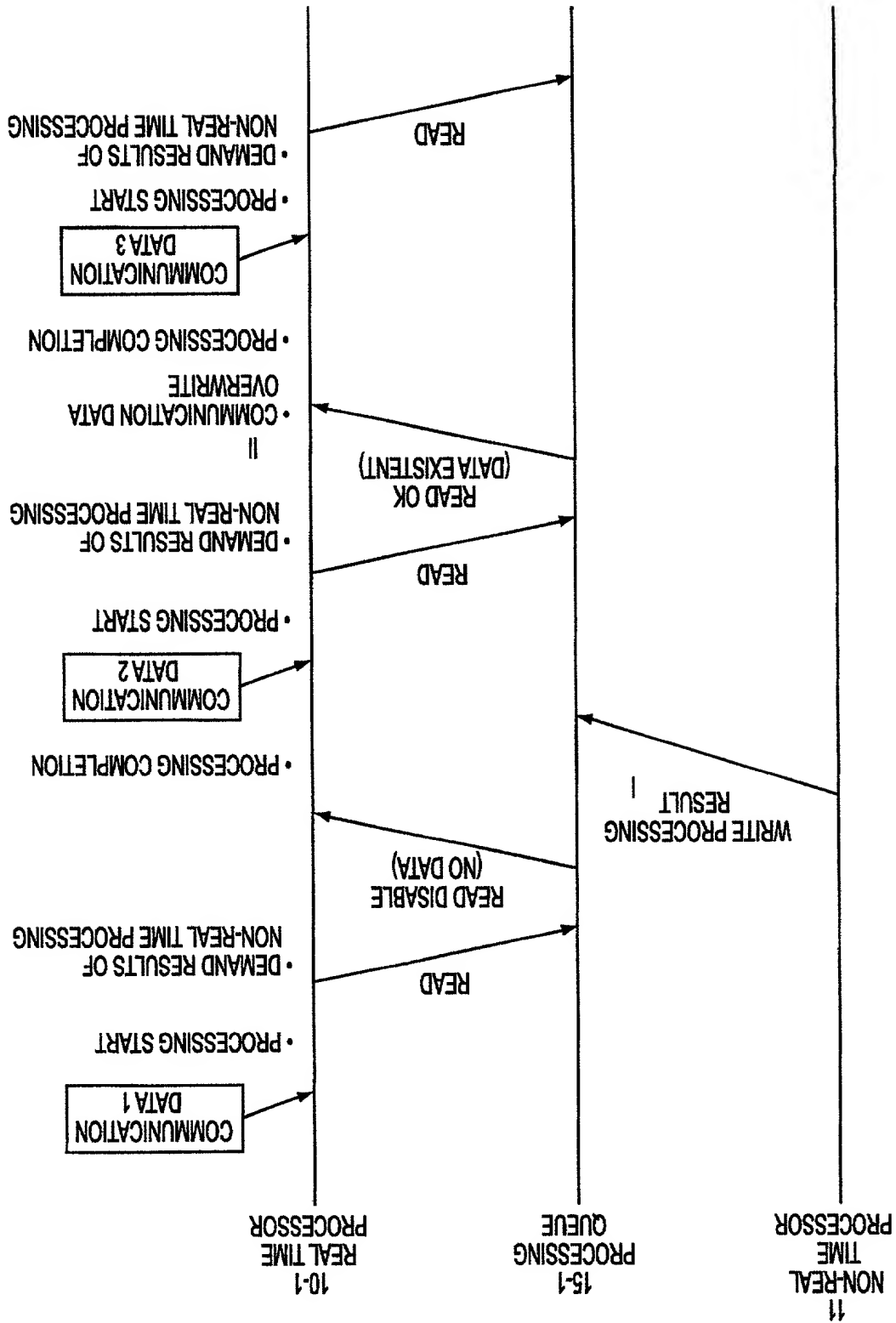


FIG. 17

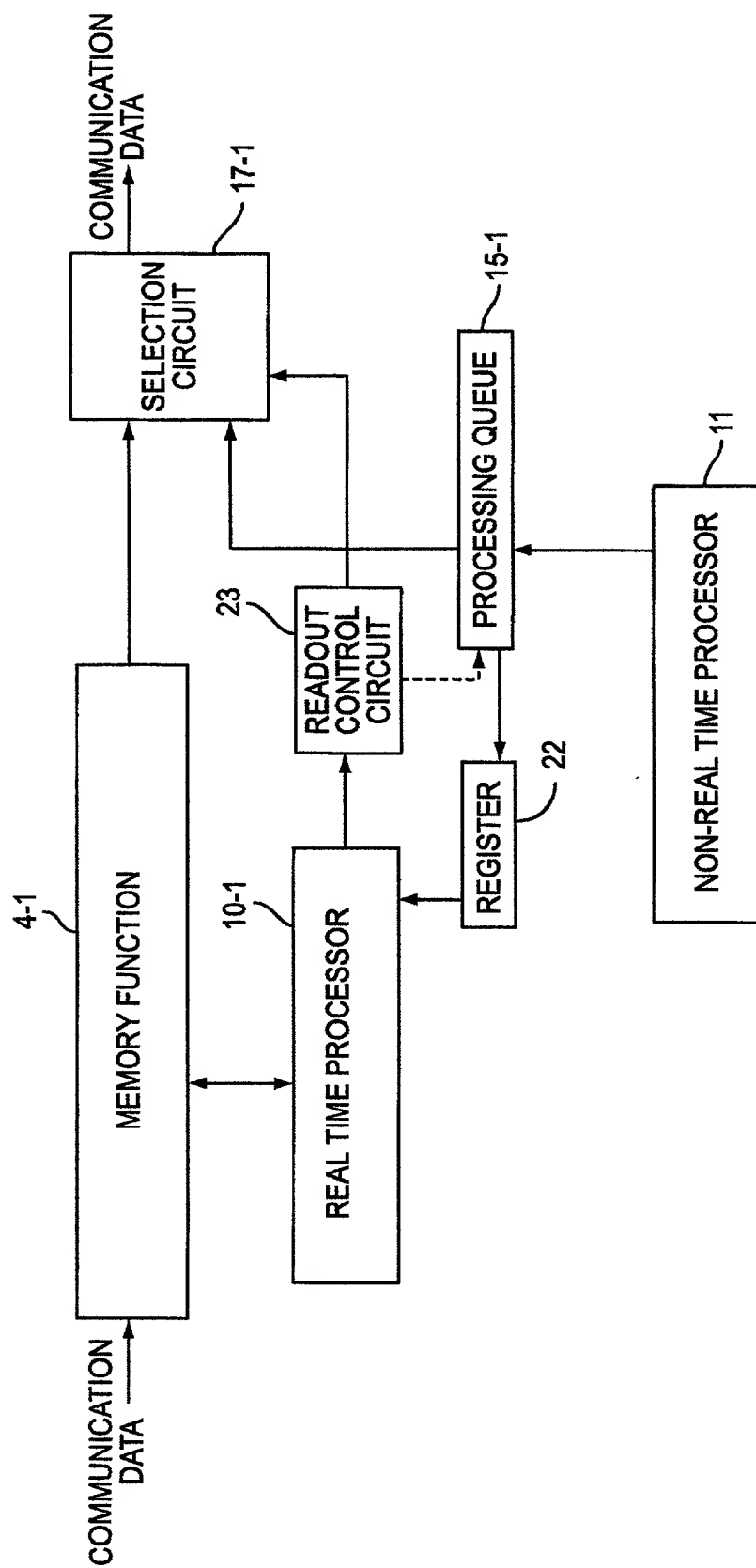


FIG. 19